

5685995

FIG. 1
(Prior Art)

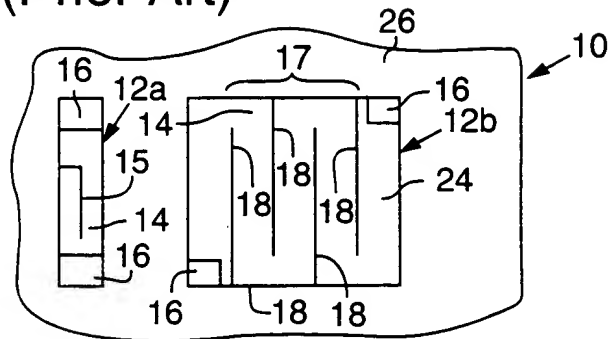


FIG. 2
(Prior Art)

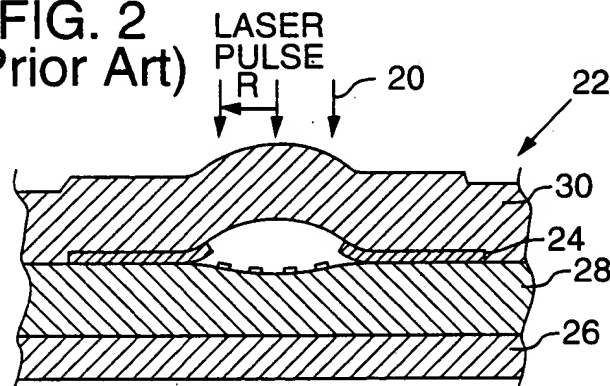
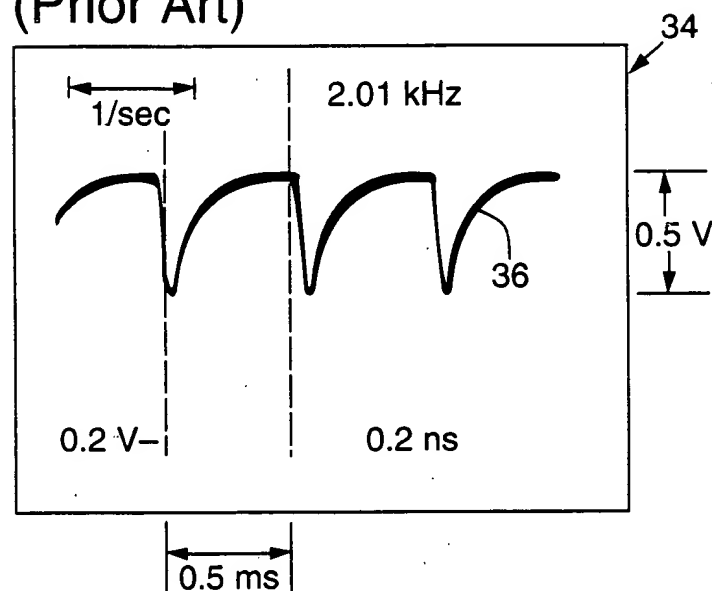


FIG. 3
(Prior Art)



APPROVED	O.G. FIG.	
BY	CLASS	SUBCLASS
DRAFTSMAN		

FIG. 4

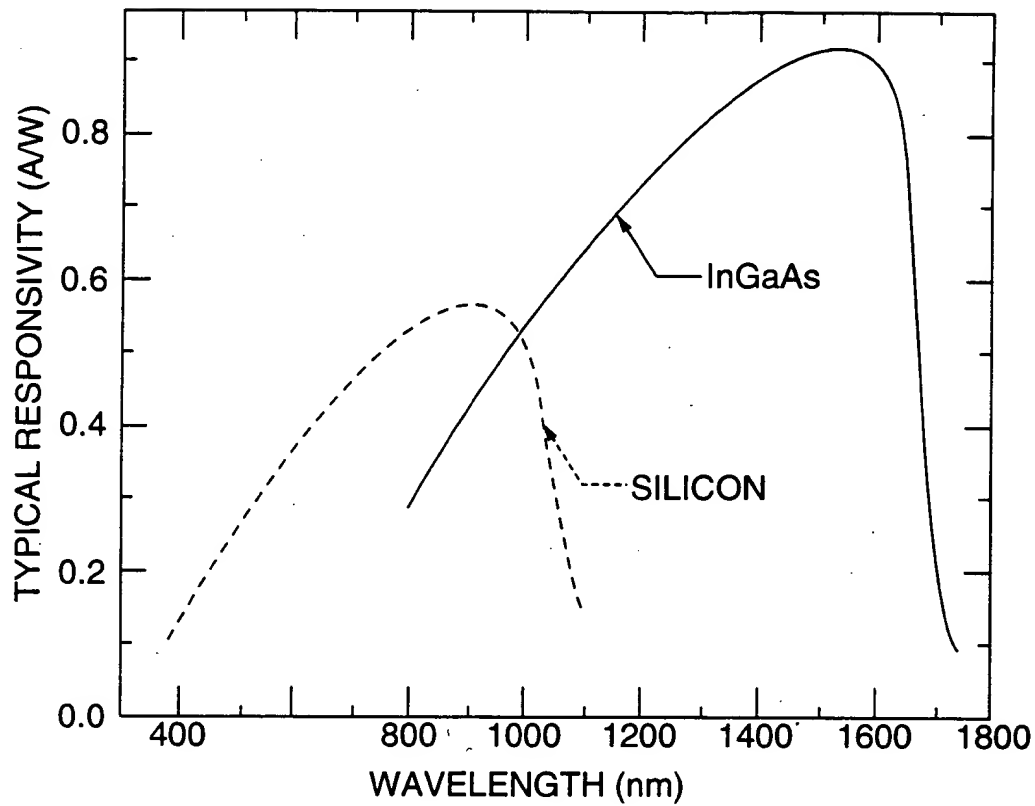
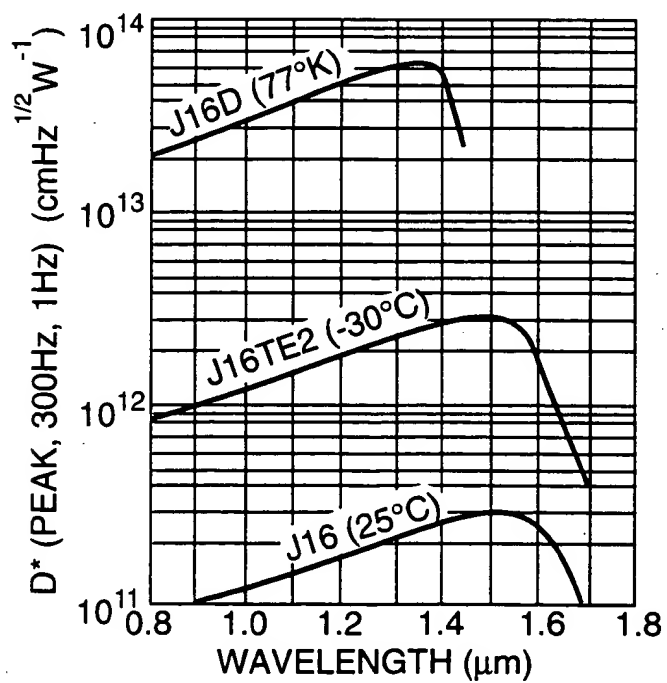


FIG. 6



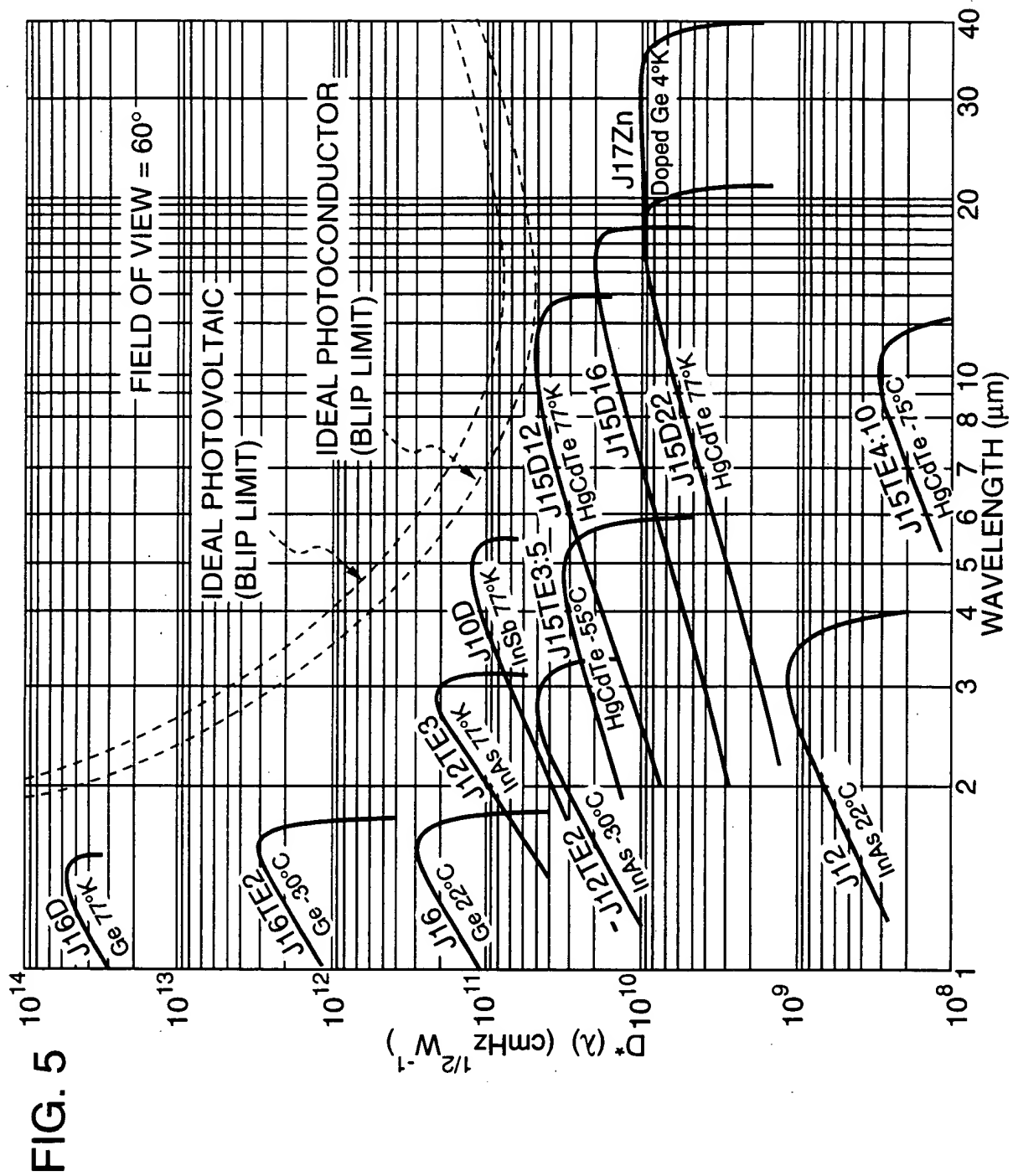
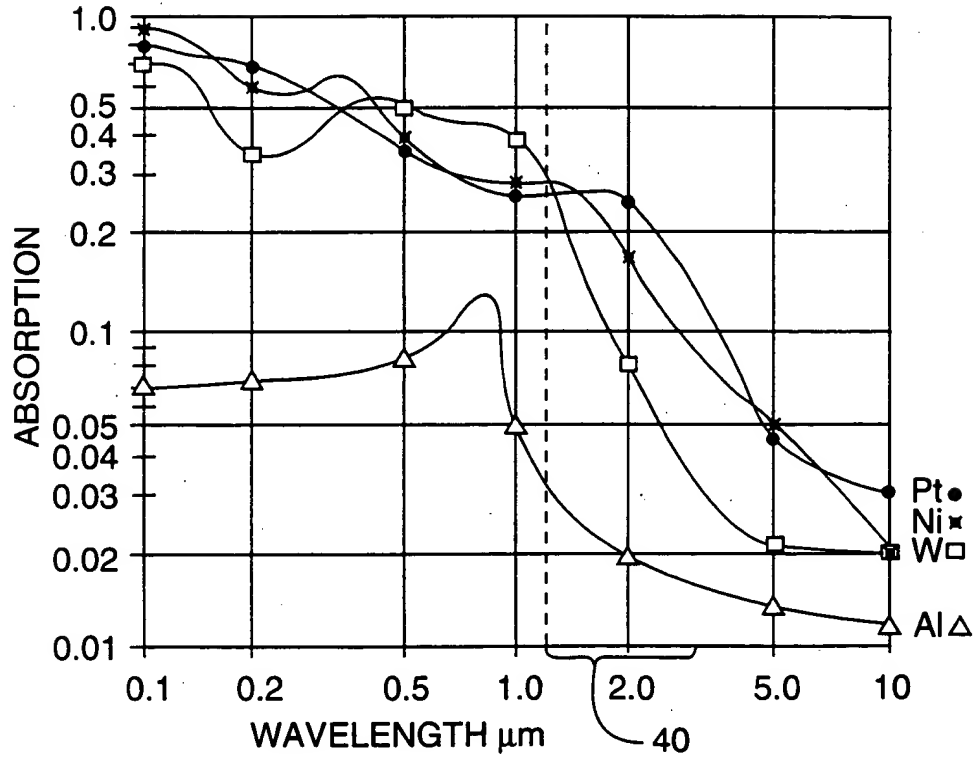


FIG. 7 OPTICAL ENERGY ABSORPTION
VS. WAVELENGTH FOR METALS



Timing diagram for the 80286 microprocessor. The diagram shows a clock signal (labeled 1/sec) and a data signal (labeled 82). The clock signal has a period of 1/sec and a high level of 0.2 V-. The data signal has a period of 0.5 ms and a high level of 0.2 ns. The diagram is labeled with 80 and 82.